B.TechIII-II Semester(ECE)

S. No.	Course Code	Subject	L	T	P	C
1.	15A52301	Managerial Economics and Financial Analysis	3	1	-	3
2.	15A04601	Microprocessors & Microcontrollers	3	1	-	3
3.	15A04602	Electronic Measurements and Instrumentation	3	1	-	3
4.	15A04603	Digital Signal Processing	3	1	-	3
5.	15A04604	VLSI Design	3	1	-	3
6.	15A04605 15A04606 15A02605 15A01608	**CBCC-I** a. MATLAB Programming b. Industrial Electronics c. Neural Networks & Fuzzy Logic d. Intellectual Property Rights	3	1	-	3
7.	15A04607	Microprocessors & Microcontrollers Laboratory	-	-	4	2
8.	15A04608	Digital Signal Processing Laboratory	-	-	4	2
9.	15A52602	Advanced English Language Communication (AELCS) Laboratory (Audit Course)	-	-	2	-
10.	15A04609	Comprehensive Online Examination-II	-	-	-	1
		Total:	**18**	**06**	**12**	**23**

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B. Tech III-IISem. (ECE)

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3	1	0	3

13A52301 MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS

Course Objectives: The objective of this course is to equip the student with the basic inputs of Managerial Economics and Economic Environment of business and to impart analytical skills in helping them take sound financial decisions for achieving higher organizational productivity.

Unit I

INTRODUCTION TO MANAGERIAL ECONOMICS

Managerial Economics – Definition- Nature- Scope - Contemporary importance of Managerial Economics - Relationship of Managerial Economics with Financial Accounting and Management. **Demand Analysis**: Concept of Demand-Demand Function - Law of Demand - Elasticity of Demand- Significance - Types of Elasticity - Measurement of elasticity of demand - Demand Forecasting- factors governing demand forecasting- methods of demand forecasting.

UNIT II

THEORY OF PRODUCTION AND COST ANALYSIS

Production Function- Least cost combination- Short-run and Long- run production function- Isoquants and Isocosts, MRTS - Cobb-Douglas production function - Laws of returns - Internal and External economies of scale - **Cost Analysis**: Cost concepts and cost behavior- Break-Even Analysis (BEA) -Determination of Break Even Point (Simple Problems)-Managerial significance and limitations of Break- Even Point.

UNIT III

INTRODUCTION TO MARKETS AND NEW ECONOMIC ENVIRONMENT

Market structures: Types of Markets - Perfect and Imperfect Competition - Features of Perfect Competition- Monopoly-Monopolistic Competition-Oligopoly-Price-Output Determination - Pricing Methods and Strategies-Forms of Business Organizations- Sole Proprietorship- Partnership – Joint Stock Companies - Public Sector Enterprises – New Economic Environment- Economic Liberalization – Privatization - Globalization.

UNIT IV

INTRODUCTION TO FINANCIAL ACCOUNTING AND ANALYSIS

Financial Accounting – Concept - Emerging need and Importance - Double-Entry Book Keeping- Journal - Ledger – Trial Balance - Financial Statements - Trading Account – Profit & Loss Account – Balance Sheet (with simple adjustments). Financial Analysis – Ratios – Liquidity, Leverage, Profitability, and Activity Ratios (simple problems).

UNIT V

CAPITAL AND CAPITAL BUDGETING

Concept of Capital - Over and Undercapitalization – Remedial Measures - Sources of Shot term and Long term Capital - Estimating Working Capital Requirements – Capital Budgeting – Features of Capital Budgeting Proposals – Methods and Evaluation of Capital Budgeting Projects – Pay Back Method – Accounting Rate of Return (ARR) – Net Present Value (NPV) – Internal Rate Return (IRR) Method (simple problems)

Learning Outcome: After completion of this course, the student will able to understand various aspects of Managerial Economics and analysis of financial statements and inputs therein will help them to make sound and effective decisions under different economic environment and market situations.

TEXT BOOKS:

1. Managerial Economics 3/e, Ahuja H.L, S.Chand, 2013.
2. Financial Management, I.M.Pandey, Vikas Publications, 2013.

REFERENCES

1. Managerial Economics and Financial Analysis, 1/e, Aryasri, TMH, 2013.
2. Managerial Economics and Financial Analysis, S.A. Siddiqui and A.S. Siddiqui, New Age International, 2013.
3. Accounting and Financial Management, T.S.Reddy& Y. Hariprasad Reddy, Margham Publishers.

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15A04601 MICROPROCESSORS AND MICROCONTROLLERS

Course Objectives:

- To understand the architecture of 8086 MICROPROCESSOR.
- To learn various 8086 Instruction set and Assembler Directives.
- To learn 8051 assembly Language programming

Course Outcomes :

After completion of this subject the students will be able to :

1. Do programming with 8086 microprocessors
2. Understand concepts of Intel x86 series of processors
3. Program MSP 430 for designing any basic Embedded System
4. Design and implement some specific real time applications Using MSP 430 low power microcontroller.

UNIT I

Introduction-8086 Architecture-Block Diagram, Register Organization, Flag Register, Pin Diagram, Timing and Control Signals, System Timing Diagrams, Memory Segmentation, Interrupt structure of 8086 and Interrupt Vector Table. Memory organization and memory banks accessing.

UNIT II

Instruction Formats -Addressing Modes-Instruction Set of 8086, Assembler Directives-Macros and Procedures.- Sorting, Multiplication, Division and multi byte arithmetic code conversion. String Manipulation instructions-Simple ALPs.

UNIT III

Low power RISC MSP430 – block diagram, features and architecture,Variants of the MSP430 family viz. MSP430x2x, MSP430x4x, MSP430x5x and their targeted applications, MSP430x5x series block diagram, Addressing modes, Instruction set Memory address space, on-chip peripherals (analog and digital), and Register sets.Sample embedded system on MSP430 microcontroller.

UNIT-IV

I/O ports pull up/down resistors concepts, Interrupts and interrupt programming. Watchdog timer. System clocks. Low Power aspects of MSP430: low power modes, Active vs Standby current consumption, FRAM vs Flash for low power & reliability.

Timer & Real Time Clock (RTC), PWM control, timing generation and measurements. Analog interfacing and data acquisition: ADC and Comparator in MSP430, data transfer using DMA.

UNIT-V:

Serial communication basics, Synchronous/Asynchronous interfaces (like UART, USB, SPI, and I2C). UART protocol, I2C protocol, SPI protocol. Implementing and programming UART, I2C, SPI interface using MSP430, Interfacing external devices. Implementing Embedded Wi-Fi using CC3100

Text Books:

1. "Microprocessor and Microcontrollers", N. Senthil Kumar, M. Saravanan, S. Jeevanathan,
   Oxford Publishers. 1 st Edition, 2010
2. "The X86 Microprocessors , Architecture, Programming and Inerfacing" , Lyla B. Das, Pearson
   Publications, 2010
3. MSP430 microcontroller basics. John H. Davies, Newnes Publication, I st Edition, 2008

References:

http://processors.wiki.ti.com/index.php/MSP430_LaunchPad_Low_Power_Mode
http://processors.wiki.ti.com/index.php/MSP430_16-Bit_Ultra-Low_Power_MCU_Training

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15A04602 ELECTRONIC MEASUREMENTS AND INSTRUMENTATION

Course Objectives:

- Studies on various analyzers and signal generators and can analyze the frequency component of a wave generated and its distortion levels.
- Studies on the difference between the various parameters which are to be measured that are getting out from the different sensors.

Course Outcomes:

After the completion of the course the students will be able to

- Understand basic principles involved in the meters for measuring voltage, current, resistance, frequency and so on.
- Employ CRO for measuring voltage, current, resistance, frequency and so on.
- Understand principles of measurements associated with different bridges.
- Get complete knowledge regarding working of advanced instruments such as logic analyzers and spectrum analyzers.

UNIT-I

Performance characteristics of Instruments: Static characteristics, Accuracy, Precision, Resolution, Sensitivity, static and dynamic calibration, Errors in Measurement, and their statistical analysis, dynamic characteristics-speed of Response, fidelity, Lag and dynamic error. DC ammeters, DC voltmeters-multirange, range extension/solid state and differential voltmeters, AC voltmeters –multirange, range extension. Thermocouple type RF ammeter, ohm meters, series type, shunt type, multimeter for voltage, current and resistance measurements.

UNIT-II

Oscilloscopes: Standard specifications of CRO,CRT features, derivation of deflection sensitivity, vertical and horizontal amplifiers, horizontal and vertical deflection systems, sweep trigger pulse, delay line, sync selector circuits, probes for CRO – active, passive, and attenuator type, triggered sweep CRO, and Delayed sweep, dual trace/beam CRO, Measurement of amplitude, frequency and phase (Lissajous method).Principles of sampling oscilloscope, storage oscilloscope, and digital storage oscilloscope, Digital frequency counters, time & Period measurements.

UNIT-III

Signal generators-fixed and variable, AF oscillators, function generators, pulse, random noise, sweep, and arbitrary waveform generators, their standards, specifications and principles of working (Block diagram approach).Wave analyzers, Harmonic distortion analyzers, Spectrum analyzers, and Logic analyzers.

UNIT-IV

Review of DC Bridges: Wheatstone bridge, Wein Bridge, errors and precautions in using bridges, AC bridges: Measurement of inductance-Maxwell's bridge, Anderson Bridge. Measurement of capacitance- SchearingBridge.Kelvin Bridge, Q-meter, EMI and EMC, Interference and noise reduction techniques.

UNIT-V

Sensors and Transducers - Active and passive transducers: Measurement of displacement (Resistance, capacitance, inductance; LVDT) Force (strain gauges) Pressure (piezoelectric transducers) Temperature (resistance thermometers, thermocouples, and thermistors), Velocity, Acceleration, Vibration, pH measurement Signal Conditioning Circuits.

TEXT BOOKS:

1. A.D. Helfrick and W.D. Cooper, "Modern Electronic Instrumentation and Measurement Techniques", PHI, 5th Edition, 2002.
2. H.S.Kalsi, "Electronic instrumentation", second edition, Tata McGraw Hill, 2004.
3. K. Lal Kishore, "Electronic Measurements & Instrumentations", Pearson Education, 2009.

REFERENCES:

1. H.S.Kalsi, "Electronic instrumentation", second edition, Tata McGraw Hill, 2004.
2. Ernest O Doebelin and Dhanesh N Manik, "Measurement Systems Application and Design", TMH, 5th Edition, 2009.
3. Oliver and Cage, "Electronic Measurement and Instrumentation", TMH.
4. Robert A.Witte, "Electronic Test Instruments, Analog and Digital Measurements", Pearson Education, 2nd Ed., 2004.
5. David A. Bell, "Electronic Instrumentation & Measurements", PHI, 2nd Edition, 2003.

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15A04603 DIGITAL SIGNAL PROCESSING

Course Objectives:

• Program a DSP chip to filter signals using either assembly language or a C compiler for the chip.

• Use Z transforms and discrete time Fourier transforms to analyze a digital system.

Course Outcomes:

At the end of the course, the student should be able to:

- Formulate engineering problems in terms of DSP tasks.
- Apply engineering problems solving strategies to DSP problems.
- Design and test DSP algorithms.
- Analyze digital and analog signals and systems.
- Analyze and compare different signal processing strategies.

UNIT-I

Review of discrete-time signals and systems – Time domain analysis of discrete-time signals & systems, Frequency domain analysis of discrete-time signals and systems.

Discrete Fourier Transform: Frequency-domain sampling and reconstruction of discrete-time signals, Discrete Fourier Transform (DFT), The DFT as a linear transformation, Relationship of the DFT to other transforms, Properties of DFT, Linear filtering methods based on DFT, Frequency analysis of signals using the DFT.

UNIT-II

Efficient computation of the DFT – Direct computation of DFT, Divide and conquer approach to computation of DFT, Radix-2, Radix-4, and Split radix FFT algorithms, Implementation of FFT algorithms, Applications of FFT algorithms – Efficient computation of the DFT of two real sequences, 2N point real sequences, Use of the FFT algorithm in linear filtering and correlation, A linear filtering approach to computation of the DFT- the Goertzel, and the Chirp-z transform algorithms, Quantization errors in the computation of DFT.

UNIT-III

Structures for the realization of discrete-time systems, Structures for FIR systems - Direct form, Cascade form, Frequency sampling, and Lattice structures, Structures for IIR systems – Direct form, Signal flow graphs & Transposed, Cascade form, Parallel form and Lattice structures, Conversion from Lattice structure to direct form, lattice – Ladder structure.

UNIT-IV

General considerations – Causality and its implications, Characteristics of practical Frequency Selective Filters, Design of Finite Impulse Response (FIR) filters – Symmetric and asymmetric FIR filters, Design of linear phase FIR filters using windows, Design of linear phase FIR filters by the frequency sampling method, Design of optimum equi-ripple linear phase FIR filters, Comparison of design methods for linear phase FIR filters, Design of Impulse Invariance Response (IIR) filters from analog filters – IIR filter design by approximation of derivatives, by Impulse invariance, and by bilinear transformation methods, Characteristics of commonly used analog filters, Design examples of both FIR and IIR filters, Frequency transformation in the analog and digital domains, Illustrative problems.

UNIT-V

Introduction, Decimation, and interpolation, Sampling rate conversion by a rational factor, Implementation of sampling rate conversion, Multistage implementation of sampling rate conversion, Sampling rate conversion of bandpass signals, Sampling rate conversion by arbitrary factor, Applications of multirate signal processing.

TEXT BOOKS:

1. John G. Proakis, Dimitris G. Manolakis, "Digital signal processing, principles, Algorithms and applications," Pearson Education/PHI, 4th ed., 2007.
2. Sanjit K Mitra, "Digital signal processing, A computer base approach," Tata McGraw Hill, 3rd edition, 2009.

REFERENCES:

1. A.V.Oppenheim and R.W. Schaffer, & J R Buck, "Discrete Time Signal Processing," 2nd ed., Pearson Education, 2012.
2. B. P. Lathi, "Principles of Signal Processing and Linear Systems," Oxford Univ. Press, 2011.
3. Li Tan, Jean Jiang, "Digital Signal Processing, Fundamentals and Applications," Academic Press, Second Edition, 2013.

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15A04604 VLSI DESIGN

Course Objectives:

- To understand VLSI circuit design processes.
- To understand basic circuit concepts and designing Arithmetic Building Blocks.
- To have an overview of Low power VLSI.

Course Outcomes:

- Complete Knowledge about Fabrication process of ICs
- Able to design VLSIcircuits as per specifications given.
- Capable of optimizingthe design of Arithmetic / logic building Blocks at all levels of Design/Fabrication.
- Can implement circuit through various design styles (semi- Custom, Full Custom)

UNIT-I

Introduction: Basic steps of IC fabrication, PMOS, NMOS, CMOS &BiCMOS,and SOI process technologies, MOS transistors - MOS transistor switches – Basic gate using switches, working polartransistor Resistors and Capacitors.

Basic Electrical Properties of MOS and BiCMOS Circuits: Working of MOS transistors – threshold voltage; MOS design equations: I_{ds}–V_{ds} relationships, Threshold Voltage, Body effect, Channel length modulation , g_m, g_{ds}, figure of merit ω_0; Pass transistor, NMOS Inverter, CMOS Inverter analysis and design, Various pull ups loads, Bi-CMOS Inverters.

UNIT-II

Basic Circuit Concepts: Capacitance, resistance estimations- Sheet Resistance R_s, MOSDivice Capacitances, routing Capacitance, Analytic Inverter Delays, Driving large Capacitive Loads, Fan-in and fan-out.

VLSI Circuit Design Processes: VLSI Design Flow, MOS Layers, Stick Diagrams, Design Rules and Layout, 2µm CMOS Design rules for wires, Contacts and Transistors Layout Diagrams for NMOS and CMOS Inverters and Gates, Scaling of MOS circuits, Limitations of Scaling.

UNIT-III

Gate level Design: Logic gates and other complex gates, Switch logic, Alternate gate circuits.

Physical Design: Floor-Planning, Placement, routing, Power delay estimation, Clock and Power routing

UNIT-IV

Subsystem Design: Shifters, Adders, ALUs, Multipliers, Parity generators, Comparators, Counters, High Density Memory Elements.

VLSI Design styles: Full-custom, Standard Cells, Gate-arrays, FPGAs, CPLDs and Design Approach for Full-custom and Semi-custom devices.

UNIT-V

VHDL Synthesis: VHDL Synthesis, Circuit Design Flow, Circuit Synthesis, Simulation, Layout, Design capture tools, Design Verification Tools.

Test and Testability: Fault-modeling and simulation, test generation, design for testability, Built-in-self-test.

TEXT BOOKS:

1. Kamran Eshraghian, Eshraghian Douglas and A. Pucknell, "Essentials of VLSI circuits and systems", PHI, 2013 Edition.
2. K.Lal Kishore and V.S.V. Prabhakar, "VLSI Design", IK Publishers

REFERENCES:

1. Weste and Eshraghian, "Principles of CMOS VLSI Design", Pearson Education, 1999.
2. Wayne Wolf, "Modern VLSI Design", Pearson Education, 3rd Edition, 1997.
3. John P. Uyemura, "Chip Design for Submicron VLSI: CMOS layout and Simulation", Thomson Learning.
4. John P. Uyemura, "Introduction to VLSI Circuits and Systems", John wiley, 2003.
5. John M. Rabaey, "Digital Integrated Circuits", PHI, EEE, 1997.

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15A04605 MATLAB PROGRAMMING
(CBCC-I)

Objectives:

- Understand the MATLAB Desktop, Command window and the Graph Window
- Be able to do simple and complex calculation using MATLAB
- Be able to carry out numerical computations and analyses
- Understand the mathematical concepts upon which numerical methods rely
- Ensure you can competently use the MATLAB programming environment
- Understand the tools that are essential in solving engineering problems

1. **UNIT-I:Introduction to MATLAB**
   MATLAB Interactive Sessions, Menus and the toolbar, computing with MATLAB, Script files and the Editor Debugger, MATLAB Help System, Programming in MATLAB.

2. **UNIT-II:Arrays**
   Arrays, Multidimensional Arrays, Element by Element Operations, Polynomial Operations Using Arrays, Cell Arrays, Structure Arrays.

3. **UNIT-III:Functions & Files**
   Elementary Mathematical Functions, User Defined Functions, Advanced Function Programming, Working with Data Files.

4. **UNIT-IV:Programming Techniques**
   Program Design and Development, Relational Operators and Logical Variables, Logical Operators and Functions, Conditional Statements, Loops, the Switch Structure, Debugging Mat Lab Programs.

Plotting :XY- plotting functions, Subplots and Overlay plots, Special Plot types, Interactive plotting, Function Discovery, Regression, 3-D plots.

5. **UNIT-V:Linear Algebraic Equations**
   Elementary Solution Methods, Matrix Methods for (Linear Equations), Cramer's Method, Undet- ermined Systems, Order Systems.

TEXT BOOKS:

1. G. H. Golub and C. F. Van Loan, Matrix Computations, 3rd Ed., Johns Hopkins University Press, 1996.
2. B. N. Datta, Numerical Linear Algebra and Applications, Brooks/Cole, 1994 (out of print) 3. L. Elden, Matrix Methods in Data Mining and Pattern Recognition, SIAM Press, 2007

Misc. Useful Information:

- NA-digest, http://www.netlib.org/na-digest-html
- Society for Industrial and Applied Mathematics (SIAM), see http://www.siam.org
- Google "MATLAB Primer" or "MATLAB Tutorial" and you should be able to access lots of free MATLAB.

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15A04606 INDUSTRIAL ELECTRONICS
(CBCC-I)

Course Outcome:
After completion of the course the students will be able to

- Get an overview of semi-conductor devices (such as PN junction diode & Transistor) and their switching characteristics.
- Understand the characteristics of AC to DC converters.
- Understand about the practical applications Electronics in industries.

UNIT I
Scope of industrial Electronics, Semiconductors, Merits of semiconductors, crystalline structure, Intrinsic semiconductors, Extrinsic semiconductors, current flow in semiconductor, Open-circuited p-n junction, Diode resistance, Zener diode, Photoconductors and junction photo diodes, Photo voltaic effect, Light emitting diodes (LED)

UNIT II
Introduction, The junction transistor, Conventions for polarities of voltages and currents, Open circuited transistor, Transistor biased in the active region, Current components in transistors, Currents in a transistor, Emitter efficiency, Transport factor and transistor-α, Dynamic emitter resistance, Transistor as an amplifier, Transistor construction, Letter symbols for semiconductor Devices, Characteristic curves of junction transistor in common configuration, static characteristic curves of PNP junction transistor in common emitter configuration, The transistor in common collector Configuration.

UNIT III
AC to DC converters- Introduction, Classification of Rectifiers, Half wave Rectifiers, Full wave Rectifiers, Comparison of Half wave and full wave rectifiers, Bridge Rectifiers, Bridge Rectifier meter, Voltage multiplying Rectifier circuits, Capacitor filter, LC Filter, Metal Rectifiers, Regulated Power Supplies, Classification of Voltage Regulators, Short period Accuracy of Regulators, Long period .Accuracy of Voltage Regulator, Principle of automatic voltage Regulator, Simple D.C. Voltage stabilizer using Zener diode, D.C. Voltage Regulators, Series Voltage Regulators, Complete series voltage regulator circuit, Simple series voltage regulator.

UNIT IV

Resistance welding controls: Introduction, Resistance welding process, Basic Circuit for A.C. resistance welding, Types of Resistance welding, Electronic welding control used in Resistance welding, Energy storage welding.**Induction heating:** Principle of induction heating, Theory of Induction heating merits of induction heating, Application of induction heating, High frequency power source of induction heating. **Dielectric heating**: Principle of dielectric heating, theory of dielectric heating, dielectric properties of typical materials, electrodes used in dielectric heating, method of coupling of electrodes to the R.F. generator, Thermal losses in Dielectric heating, Applications.

UNIT V :

Ultrasonics: Introduction, Generation of Ultrasonic waves, Application of Ultrasonic waves, Ultrasonic stroboscope, ultrasonic as means of communication, ultrasonic flaw detection, Optical image on non-homogeneities, ultrasonic study of structure of matter, Dispersive study of structure of matter, Dispersive and colloidal effect of Ultrasonic, Coagulating action of Ultrasonic, separation of mixtures by ultrasoni8c waves, cutting and machining of hard materials by ultrasonic vibrations, Degassing of liquids by ultrasonic waves, Physico-chemical effects of ultrasonics, chemical effects of ultrasonics, Thermal effects of Ultrasonics, soldering and welding by ultrasonics, Ultrasonic Drying

Text Books:

1. G. K. Mithal, “Industrial Electronics”, Khanna Publishers, Delhi, 2000.
2. J.Gnanavadivel, R.Dhanasekaran, P.Maruthupandi, “Industrial Electronics”, Anuradha Publications, 2011.

Reference Books:

1. F. D. Petruzulla, “Industrial Electronics”, McGraw Hill, Singapore, 1996.
2. M. H. Rashid, “power Electronics Circuits, Devices and Application”, PHI, 3rd edition, 2004.
3. G. M. Chute and R. D. Chute, “Electronics in Industry”, McGraw Hill Ltd, Tokyo, 1995.

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3	1	0	3

15A02605 NEURAL NETWORKS & FUZZY LOGIC
(CBCC-I)

Course Objectives:

- To analyze basic neural computational models.
- To get in detail knowledge regarding different algorithms related to neural learning
- To study about different issues related probability and fuzziness and different types of fuzzy associative memories.

Course Outcomes:

After completion of the course the students will be able to

- Get an overview of different types of neural network models.
- Understand the functioning of single; multi-layer feed forward neural networks, associative memories and their rules and algorithms.
- Understand about fundamentals of fuzzy logic, their rules and applications.

UNIT I

Introduction to Neural Networks: Biological neuron, McCulloh-pitts neuron model, Neuron Modelling for Artificial Neural Systems, Models of Artificial Neural Networks-feedforward and feedback networks, Neural Processing, Learning as approximation, Supervised and unsupervised learning, Neural Network Learning rules- Hebbian, Perceptron, Delta, Widrow-Hoff, Correlation, Winner-Take-All learning rules.

UNIT II

Single-Layer Neural Networks: Classification Model, Features and Decision Regions, Discriminant Functions, Linear Machine and Minimum Distance Classification, Training and Classification using Discrete Perceptron, Single-Layer Continuous Perceptron Networks, Multicategory Single-Layer Perceptron Networks, Hopfield Network – Discrete-time, Gradient type.

Multi-Layer Neural Networks: Linearly Nonseparable Pattern Classification, Delta Learning Rule for Multiperceptron Layer, Generalized Delta Learning Rule, Feed forward Recall and Error Back-propagation training, Learning Factors.

UNIT III

Associative Memories: Basic concepts, Linear Associator, Recurrent Auto associate Memory, Performance Analysis of Recurrent Auto associate Memory, Bidirectional Associate Memory(BAM): Memory Architecture, Association Encoding and Decoding, Stability Considerations, Memory Example and Performance Evaluation, Improved coding of memories, Multidirectional Associative Memory, Associative Memory of Spatial-Temporal Patterns.

UNIT IV

Fuzzy Set– Introduction: Basic concepts of fuzzy logic, Fuzzy sets and Crisp sets, Fuzzy set theory and operations, Properties of fuzzy sets, Fuzzy and Crisp relations, Fuzzy to Crisp conversion.

UNIT V

Fuzzy Logic - Fuzzy Membership, Rules: Membership functions, interference in fuzzy logic, fuzzy if-then rules, Fuzzy implications and Fuzzy algorithms, Fuzzyfications&Defuzzificataions, Fuzzy Controller, Industrial applications.

Text Books:

1. JacekM.Zurada," Introdution to Artificial Neural Systems", West Publishing Company
2. Timothy J.Ross, " Euzzy Logic with Engineering Applications", Wiley Indian 3rd Edition

Reference Books:

1. George J.Klir/Bo Yuan, "Fuzzy Sets and Fuzzy Logic : Theory and apllications", Prentice-Hall Edition
2. S.N.Sivanandam, S.Sumathi, S.N.Deepa, "Introduction to Neural Networks using MATLAB 6.0", TMH, 2006.
3. S.N.Sivanandam, S.Sumathi, S.N.Deepa, "Introduction to Fuzzy Logic using MATLAB 6.0", TMH, 2006
4. Simon Haykins, "Neural Networks", Pearson Education.

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15A01608 INTELLECTUAL PROPERTY RIGHTS
(CBCC – I)

COURSE OBJECTIVE:

This course introduces the student to the basics of Intellectual Property Rights, Copy Right Laws Trade Marks and Issues related to Patents. The overall idea of the course is to help and encourage the student for startups and innovations.

UNIT – I

Introduction To Intellectual Property: Introduction, Types Of Intellectual Property, International Organizations, Agencies And Treaties, Importance Of Intellectual Property Rights.

UNIT – II

Trade Marks : Purpose And Function Of Trade Marks, Acquisition Of Trade Mark Rights, Protectable Matter, Selecting And Evaluating Trade Mark, Trade Mark Registration Processes.

UNIT – III

Law Of Copy Rights : Fundamental Of Copy Right Law, Originality Of Material, Rights Of Reproduction, Rights To Perform The Work Publicly, Copy Right Ownership Issues, Copy Right Registration, Notice Of Copy Right, International Copy Right Law.
Law Of Patents : Foundation Of Patent Law, Patent Searching Process, Ownership Rights And Transfer

UNIT – IV

Trade Secrets : Trade Secrete Law, Determination Of Trade Secrete Status, Liability For Misappropriations Of Trade Secrets, Protection For Submission, Trade Secrete Litigation.
Unfair Competition : Misappropriation Right Of Publicity, False Advertising.

UNIT – V

New Developments Of Intellectual Property: New Developments In Trade Mark Law ; Copy Right Law, Patent Law, Intellectual Property Audits.
International Overview On Intellectual Property, International – Trade Mark Law, Copy Right Law, International Patent Law, International Development In Trade Secrets Law.

TEXT BOOKS & REFERENCES:

1. Intellectual Property Rights, Deborah. E. Bouchoux, Cengage Learing.
2. Intellectual Property Rights– UnleashmyThe Knowledge Economy, PrabuddhaGanguli, Tate Mc Graw Hill Publishing Company Ltd.,

Course Outcomes:

On completion of this course, the student will have an understanding of the following:

a) *Intellectual Property Rights and what they mean*
b) *Trade Marks and Patents and how to register them*
c) *Laws Protecting the Trade Marks and Patents*
d) *Copy Right and laws related to it.*

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15A04607 MICROPROCESSORS AND MICROCONTROLLERS LABORATORY

Part A : 8086 Microprocessor Programs using NASM/8086 microprocessor kit.

1. Introduction to MASM Programming.
2. Programs using arithmetic and logical operations
3. Programs using string operations and Instruction prefix: Move Block, Reverse string, Sorting, String comparison
4. Programs for code conversion
5. Multiplication and Division programs
6. Sorting and multi byte arithmetic
7. Programs using CALL and RET instructions

Part B Embedded C Experiments using MSP430 Microcontroller

1. Interfacing and programming GPIO ports in C using MSP430 (blinking LEDs , push buttons)
2. Usage of Low Power Modes: (Use MSPEXP430FR5969 as hardware platform and demonstrate the low power modes and measure the active mode and standby mode current)
3. Interrupt programming examples through GPIOs
4. PWM generation using Timer on MSP430 GPIO
5. Interfacing potentiometer with MSP430
6. PWM based Speed Control of Motor controlled by potentiometer connected to MSP430 GPIO
7. Using ULP advisor in Code Composer Studio on MSP430
8. Low Power modes and Energy trace++:
    a. Enable Energy Trace and Energy Trace ++ modes in CCS
    b. Compute Total Energy, and Estimated lifetime of an AA battery.

Note : Any six experiment from Part A and Six experiments from Part B are to be conducted

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15A04608 DIGITAL SIGNAL PROCESSING LABORATORY

Course Outcomes:

- Able to design real time DSP systems and real world applications.
- Able to implement DSP algorithms using both fixed and floating point processors.

List of Experiments: (Minimum of 5 experiments are to be conducted from each part)Software Experiments (PART – A)

1. Generation of random signal and plot the same as a waveform showing all the specifications.
2. Finding Power and (or) Energy of a given signal.
3. Convolution and Correlation (auto and cross correlation) of discrete sequences without using built in functions for convolution and correlation operations.
4. DTFT of a given signal
5. N – point FFT algorithm
6. Design of FIR filter using windowing technique and verify the frequency response of the filter.
7. Design of IIR filter using any of the available methods and verify the frequency response of the filter.
8. Design of analog filters.

Using DSP Processor kits (Floating point) and Code Composure Studio (CCS) (PART – B)

1. Generation of random signal and plot the same as a waveform showing all the specifications.
2. Finding Power and (or) Energy of a given signal.
3. Convolution and Correlation (auto and cross correlation) of discrete sequences without using built in functions for convolution and correlation operations.
4. DTFT of a given signal
5. N – point FFT algorithm
6. Design of FIR filter using windowing technique and verify the frequency response of the filter.
7. Design of IIR filter using any of the available methods and verify the frequency response of the filter.
8. Design of analog filters.

Equipment/Software Required:

1. Licensed MATLAB software with required tool boxes for 30 users.
2. DSP floating Processor Kits with Code Composure Studio (8 nos.)
3. Function generators
4. CROs
5. Regulated Power Supplies.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

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15A52602 ADVANCED ENGLISH LANGUAGE COMMUNICATION SKILLS (AELCS) LAB (Audit Course)

1. INTRODUCTION

With increased globalization and rapidly changing industry expectations, employers are looking for the wide cluster of skills to cater to the changing demand. The introduction of the Advanced Communication Skills Lab is considered essential at 3rd year level. At this stage, the students need to prepare themselves for their careers which may require them to listen to, read, speak and write in English both for their professional and interpersonal communication in the globalised context.

The proposed course should be a laboratory course to enable students to use 'good' English and perform the following:

- Gathering ideas and information and to organise ideas relevantly and coherently.
- Engaging in debates.
- Participating in group discussions.
- Facing interviews.
- Writing project/research reports/technical reports.
- Making oral presentations.
- Taking part in social and professional communication.

2. OBJECTIVES:

This Lab focuses on using multi-media instruction for language development to meet the following targets:

- To improve the students' fluency in English, through a well-developed vocabulary and enable them to listen to English spoken at normal conversational speed by educated English speakers and respond appropriately in different socio-cultural and professional contexts.
- Further, they would be required to communicate their ideas relevantly and coherently in writing.
- To prepare all the students for their placements.

3. SYLLABUS:

The following course content to conduct the activities is prescribed for the Advanced English Communication Skills (AECS) Lab:

UNIT-I: COMMUNICATION SKILLS

1. Reading Comprehension
2. Listening comprehension
3. Vocabulary Development
4. Common Errors

UNIT-II: WRITING SKILLS

1. Report writing
2. Resume Preparation
3. E-mail Writing

UNIT-III: PRESENTATION SKILLS

1. Oral presentation
2. Power point presentation
3. Poster presentation

UNIT-IV: GETTING READY FOR JOB

1. Debates
2. Group discussions
3. Job Interviews

UNIT-V: INTERPERSONAL SKILLS

1. Time Management
2. Problem Solving & Decision Making
3. Etiquettes

4. LEARNING OUTCOMES:

- Accomplishment of sound vocabulary and its proper use contextually
- Flair in Writing and felicity in written expression.
- Enhanced job prospects.
- Effective Speaking Abilities

5. MINIMUM REQUIREMENT:

The Advanced English Communication Skills (AECS) Laboratory shall have the following infra-structural facilities to accommodate at least 60 students in the lab:

- Spacious room with appropriate acoustics.
- Round Tables with movable chairs
- Audio-visual aids
- LCD Projector
- Public Address system
- P – IV Processor, Hard Disk – 80 GB, RAM–512 MB Minimum, Speed – 2.8 GHZ
- T. V, a digital stereo & Camcorder
- Headphones of High quality

6. SUGGESTED SOFTWARE:

The software consisting of the prescribed topics elaborated above should be procured and G

1. **Walden Infotech: Advanced English Communication Skills Lab**
2. **K-VAN SOLUTIONS-Advanced English Language Communication Skills lab**
3. **DELTA's key to the Next Generation TOEFL Test: Advanced Skills Practice.**
4. **TOEFL & GRE**(KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)
5. **Train2success.com**

7. BOOKS RECOMMENDED:

1. **Objective English for Competitive Exams**, Hari Mohana Prasad, 4th edition, Tata Mc Graw Hill.
2. **Technical Communication** by Meenakshi Raman & Sangeeta Sharma, O U Press 3rdEdn. 2015.
3. **Essay Writing for Exams, AudroneRaskauskiene, Irena Ragaisience&RamuteZemaitience,OUP, 2016**
4. **Soft Skills for Everyone,** Butterfield Jeff, Cengage Publications, 2011.
5. **Management Shapers Series** by Universities Press (India) Pvt Ltd., Himayatnagar, Hyderabad 2008.
6. **Campus to Corporate,** Gangadhar Joshi, Sage Publications, 2015
7. **Communicative English,**E Suresh Kumar &P.Sreehari, Orient Blackswan, 2009.
8. **English for Success in Competitive Exams,** Philip Sunil Solomon OUP, 2015