

B.Tech III-II Semester (EEE)

| S. No. | Course Code | Subject | L | T | P | C |
|---------------|--|--|-----------|----------|-----------|-----------|
| 1. | 15A52601 | Management Science | 3 | 1 | - | 3 |
| 2. | 15A02601 | Power Semiconductor Drives | 3 | 1 | - | 3 |
| 3. | 15A02602 | Power System Protection | 3 | 1 | - | 3 |
| 4. | 15A04601 | Microprocessors & Microcontrollers | 3 | 1 | - | 3 |
| 5. | 15A02603 | Power System Analysis | 3 | 1 | - | 3 |
| 6. | 15A02604 15A02605 15A02606 15A01608 | CBCC -I 1) Neural Networks & Fuzzy Logic 2) Programmable Logic Controller & Its Applications 3) Optimization Techniques 4) Intellectual Property Rights | 3 | 1 | - | 3 |
| 7. | 15A04607 | Microprocessors & Microcontrollers Laboratory | - | - | 4 | 2 |
| 8. | 15A02607 | Power Electronics & Simulation Laboratory | - | - | 4 | 2 |
| 9. | 15A52602 | Advanced English Language Communication Skills (AELCS) Laboratory (Audit Course) | - | - | 2 | - |
| 10. | 15A02608 | Comprehensive Online Examination - II | - | - | - | 1 |
| Total: | | | 18 | 6 | 12 | 23 |

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR
B. Tech III-II Sem. (EEE)

| L | T | P | C |
|---|---|---|---|
| 3 | 1 | 0 | 3 |

15A52601 MANAGEMENT SCIENCE

Course Objective: The objective of the course is to equip the student the fundamental knowledge of management science and its application for effective management of human resource, materials and operation of an organization. It also aims to expose the students about the latest and contemporary developments in the field of management.

UNIT –I:

Introduction to Management: Concept-Nature and Importance of Management, Functions-Evaluation of Scientific Management, Modern management-Motivation Theories-Leadership Styles-Decision Making Process-Designing Organization Structure-Principles and Types of Organization.

UNIT- II:

Operations Management: Plant location and Layout, Methods of production, Work-Study-Statistical Quality Control through Control Charts, Objectives of Inventory Management, Need for Inventory Control-EOQ&ABC Analysis(Simple Problems)**Marketing Management:** Meaning,Nature, Functions of Marketing, Marketing Mix, Channels of distribution- Advertisement and sales promotion-Marketing strategies-Product Life Cycle.

UNIT -III:

Human Resource Management(HRM): Significant and Basic functions of HRM- Human Resource Planning(HRP), Job evaluation, Recruitment and Selection, Placement and Induction-Wage and Salary administration. Employee Training and development-Methods-Performance Appraisal-Employee Grievances-techniques of handling Grievances.

UNIT –IV:

Strategic Management: Vision, Mission, Goals and Strategy- Corporate Planning Process-Environmental Scanning-SWOT analysis-Different Steps in Strateg Formulation, Implementation and Evaluation. **Project Management:** Network Analysis- PERT, CPM, Identifying Critical Path-Probability-Project Cost Analysis, Project Crashing (Simple Problems).

UNIT-V:

Contemporary Management Practices: Basic concepts of MIS-Materials Requirement Planning(MRP),Just-In-Time(JIT)System, Total Quality Management(TQM)-Six Sigma

and Capability Maturity Models(CMM) evies, Supply Chain Management, Enterprise Resource Planning(ERP),Performance Management, Business Process Outsourcing(BPO), Business Process Re-Engineering and Bench Marking, Balance Score Card.

Course Outcome: This course enables the student to know the principles and applications of management knowledge and exposure to the latest developments in the field. This helps to take effective and efficient management decisions on physical and human resources of an organization. Beside the knowledge of Management Science facilitates for his/her personal and professional development.

TEXT BOOKS:

1. A.R Aryasri: Management Science, TMH, 2013
2. Kumar /Rao/Chalill 'Introduction to Management Science' Cengage, Delhi, 2012.

REFERENCE BOOKS:

1. A.K.Gupta "Engineering Management",S.CHAND, New Delhi, 2016.
2. Stoner, Freeman, Gilbert, Management, Pearson Education,New Delhi, 2012.
3. Kotler Philip & Keller Kevin Lane: Marketing Mangement , PHI,2013.
5. Koontz & Weihrich: Essentials of Management, 6/e, TMH, 2005.
6. Kanishka Bedi, Production and Operations Management, Oxford University Press, 2004.
7. Memoria & S.V.Gauker, Personnel Management, Himalaya, 25/e, 2005
8. Parnell: Strategic Management, Biztantra, 2003.
9. L.S.Srinath: PERT/CPM,Affiliated East-West Press, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B. Tech III-II Sem. (EEE)

| L | T | P | C |
|---|---|---|---|
| 3 | 1 | 0 | 3 |

15A02601 POWER SEMICONDUCTOR DRIVES

Course Objectives: The objectives of the course are to make the students learn about:

- The operation of electric motor drives controlled by power electronic converters.
- The stable steady-state operation and transient dynamics of a motor-load system.
- The operation of the chopper fed DC drive.
- The distinguishing features of synchronous motor drives and induction motor drives.

UNIT – I

CONVERTER FED DC MOTORS

Classification of Electric Drives, Basic elements of Electric Drive, Dynamic Control of a Drive system, Stability analysis, Introduction to Thyristor Controlled Drives, Single Phase, Three Phase Semi and Fully Controlled Converters Connected to D.C Separately Excited and D.C Series Motors – Continuous Current Operation – Output Voltage and Current Waveforms – Speed and Torque Expressions – Speed – Torque Characteristics- Problems.

UNIT – II

FOUR QUADRANT OPERATION OF DC DRIVES

Introduction to Four Quadrant Operation – Motoring Operations, Electric Braking – Plugging, Dynamic and Regenerative Braking Operations. Four Quadrant Operation of D.C Motors by Dual Converters – Closed Loop Operation of DC Motor (Block Diagram Only)

UNIT – III

CHOPPER FED DC MOTORS

Single Quadrant, Two Quadrant and Four Quadrant Chopper Fed DC Separately Excited and Series Excited Motors – Continuous Current Operation – Output Voltage and Current Wave Forms – Speed Torque Expressions – Speed Torque Characteristics – Problems on Chopper Fed D.C Motors

UNIT – IV

CONTROL OF INDUCTION MOTOR

Induction Motor Stator Voltage Control and Characteristics. AC Voltage Controllers – Waveforms – Speed Torque Characteristics - Stator Frequency Control and Characteristics. Voltage Source and Current Source Inverter - PWM Control – Comparison of VSI and CSI Operations – Speed Torque Characteristics – Numerical Problems on Induction Motor Drives – Closed Loop Operation of Induction Motor Drives (Block Diagram Only) – Principles of Vector Control

Static Rotor Resistance Control – Slip Power Recovery – V/f control of Induction Motor – Their Performance and Speed Torque Characteristics – Advantages- Applications – Problems

UNIT – V

CONTROL OF SYNCHRONOUS MOTORS

Separate Control & Self Control of Synchronous Motors – Operation of Self Controlled Synchronous Motors by VSI and CSI Cycloconverters. Load Commutated CSI Fed Synchronous Motor – Operation – Waveforms – Speed Torque Characteristics – Applications – Advantages and Numerical Problems – Closed Loop Control Operation of Synchronous Motor Drives (Block Diagram Only), Introduction to variable frequency control.

Course Outcomes: The student should be able to:

- Identify the choice of the electric drive system based on their applications
- Explain the operation of single and multi quadrant electric drives
- Analyze single phase and three phase rectifiers fed DC motors as well as chopper fed DC motors
- Explain the speed control methods for AC-AC & DC-AC converters fed to Induction motors and Synchronous motors with closed loop, and open loop operations.

TEXT BOOKS:

1. Power semiconductor controlled drives, G K Dubey, Prentice Hall, 1995.
2. Modern Power Electronics and AC Drives, B.K.Bose, PHI, 2002.

REFERENCE BOOKS:

1. Power Electronics, MD Singh and K B Khanchandani, Tata McGraw-Hill Publishing company, 2008.
2. Power Electronic Circuits, Devices and applications, M.H.Rashid, PHI, 2005.
3. Electric drives Concepts and Applications, Vedam Subramanyam, Tata McGraw Hill Publications, 2nd Edition, 2011.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B. Tech III-II Sem. (EEE)

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 3 | 1 | 0 | 3 |

15A02602 POWER SYSTEM PROTECTION

Course Objectives: The objectives of the course are to make the students learn about:

- The different types of electromagnetic relays and microprocessor based relays
- The protection of Generators
- The protection of Transformers
- The protection of feeders and lines
- The technical aspects involved in the operation of circuit breakers
- Generation of over voltages and protection from over voltages

UNIT – I

RELAYS

Electromagnetic Relays - Basic Requirements of Relays – Primary and Backup Protection - Construction Details of – Attracted Armature, Balanced Beam, Inductor Type and Differential Relays – Universal Torque Equation – Characteristics of Over Current, Direction and Distance Relays. Static Relays – Advantages and Disadvantages – Definite Time, Inverse and IDMT. Static Relays – Comparators – Amplitude and Phase Comparators. Microprocessor Based Relays – Advantages and Disadvantages – Block Diagram for Over Current (Definite, Inverse and IDMT) and Distance Relays and Their Flow Charts.

UNIT – II

PROTECTION OF GENERATORS & TRANSFORMERS

Protection of Generators Against Stator Faults, Rotor Faults and Abnormal Conditions. Restricted Earth Fault and Inter-Turn Fault Protection. Numerical Problems on percentage winding unprotected. Protection of Transformers: Percentage Differential Protection, Numerical Problems on Design of CT Ratio, Buchholtz Relay Protection, Numerical Problems.

UNIT – III

PROTECTION OF FEEDERS & LINES

Protection of Feeder (Radial & Ring Main) Using Over Current Relays. Protection of Transmission Line – 3 Zone Protection Using Distance Relays. Carrier Current Protection. Protection of Bus Bars.

UNIT – IV

CIRCUIT BREAKERS

Circuit Breakers: Elementary Principles of Arc Interruption, Restriking Voltage and Recovery Voltage - Restriking Phenomenon, Average and Max. RRRV, Numerical Problems - Current Chopping and Resistance Switching - CB Ratings and Specifications: Types and Numerical Problems. – Auto Reclosures. Description and Operation of Following Types of Circuit Breakers: Minimum Oil Circuit Breakers, Air Blast Circuit Breakers, Vacuum and SF6 Circuit Breakers.

UNIT – V

OVER VOLTAGES IN POWER SYSTEMS

Generation of Over Voltages in Power Systems.-Protection against Lightning Over Voltages - Valve Type and Zinc-Oxide Lightning Arresters - Insulation Coordination –BIL.

Course Outcomes: At the end of the course the student should be able to:

- Explain the principles of operation of various types of electromagnetic relays, Static relays as well as Microprocessor based relays
- Understanding the protection of generators and determination of what % generator winding is unprotected under fault occurrence
- Understanding the protection of transformers and make design calculations to determine the required CT ratio for transformer protection
- Explain the use of relays in protecting Feeders, lines and bus bars
- Solve numerical problems concerning the arc interruption and recovery in circuit breakers
- Understand why over voltages occur in power system and how to protect the system

TEXT BOOKS:

1. Power System Protection and Switchgear, Badri Ram, D.N Viswakarma, TMH Publications, 2011.
2. Switchgear and Protection, Sunil S Rao, Khanna Publishers, 1992.

REFERENCE BOOKS:

1. Electrical Power Systems, C.L.Wadhwa, New Age international (P) Limited, Publishers, 2012.
2. Transmission network Protection, Y.G. Paithankar ,Taylor and Francis,2009.
3. Power system protection and switch gear, Bhuvanesh Oza, TMH, 2010.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B. Tech III-II Sem. (EEE)

| L | T | P | C |
|---|---|---|---|
| 3 | 1 | 0 | 3 |

15A04601 MICROPROCESSORS AND MICROCONTROLLERS

Course Outcomes:

After completion of this subject the students will be able to :

1. Do programming with 8086 microprocessors
2. Understand concepts of Intel x86 series of processors
3. Program MSP 430 for designing any basic Embedded System
4. Design and implement some specific real time applications Using MSP 430 low power microcontroller.

UNIT I

Introduction-8086 Architecture-Block Diagram, Register Organization, Flag Register, Pin Diagram, Timing and Control Signals, System Timing Diagrams, Memory Segmentation, Interrupt structure of 8086 and Interrupt Vector Table. Memory organization and memory banks accessing.

UNIT II

Instruction Formats -Addressing Modes-Instruction Set of 8086, Assembler Directives-Macros and Procedures.- Sorting, Multiplication, Division and multi byte arithmetic code conversion. String Manipulation instructions-Simple ALPs.

UNIT III

Low power RISC MSP430 – block diagram, features and architecture,Variants of the MSP430 family viz. MSP430x2x, MSP430x4x, MSP430x5x and their targeted applications, MSP430x5x series block diagram, Addressing modes, Instruction set Memory address space, on-chip peripherals (analog and digital), and Register sets.Sample embedded system on MSP430 microcontroller.

UNIT-IV

I/O ports pull up/down resistors concepts, Interrupts and interrupt programming. Watchdog timer. System clocks. Low Power aspects of MSP430: low power modes, Active vs Standby current consumption, FRAM vs Flash for low power & reliability. Timer & Real Time Clock (RTC), PWM control, timing generation and measurements. Analog interfacing and data acquisition: ADC and Comparator in MSP430, data transfer using DMA.

UNIT-V

Serial communication basics, Synchronous/Asynchronous interfaces (like UART, USB, SPI, and I2C). UART protocol, I2C protocol, SPI protocol. Implementing and programming UART, I2C, SPI interface using MSP430, Interfacing external devices. Implementing Embedded Wi-Fi using CC3100

Text Books:

1. "Microprocessor and Microcontrollers", N. Senthil Kumar, M. Saravanan, S. Jeevanathan, Oxford Publishers. 1 st Edition, 2010
2. "The X86 Microprocessors , Architecture, Programming and Inerfacing" , Lyla B. Das, Pearson Publications, 2010
3. MSP430 microcontroller basics. John H. Davies, Newnes Publication, I st Edition, 2008

References:

http://processors.wiki.ti.com/index.php/MSP430_LaunchPad_Low_Power_Mode
http://processors.wiki.ti.com/index.php/MSP430_16-Bit_Ultra-Low_Power_MCU_Training

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B. Tech III-II Sem. (EEE)

| | | | |
|----------|----------|----------|----------|
| L | T | P | C |
| 3 | 1 | 0 | 3 |

15A02603 POWER SYSTEM ANALYSIS

Course Objectives: The objectives of the course are to make the students learn about:

- Y bus and Z bus of a Power System network
- Power flow studies by various methods.
- Short circuit analysis of power systems.
- Swing equation and its solution
- Equal area criterion and its applications

UNIT -I

POWER SYSTEM NETWORK MATRICES

Representation of Power System Elements, Graph Theory: Definitions, Bus Incidence Matrix, Y_{bus} Formation by Direct and Singular Transformation Methods, Numerical Problems. Formation of Z_{Bus} : Partial Network, Algorithm for the Modification of Z_{Bus} Matrix for Addition Element for the Following Cases: Addition of Element from a New Bus to Reference, Addition of Element from a New Bus to an Old Bus, Addition of Element Between an Old Bus to Reference and Addition of Element Between Two Old Busses (Derivations and Numerical Problems).- Modification of Z_{Bus} for the Changes in Network (Problems)

UNIT – II

SHORT CIRCUIT ANALYSIS

Per-Unit System of Representation. Per-Unit Equivalent Reactance Network of a Three Phase Power System, Numerical Problems. Symmetrical Fault Analysis: Short Circuit Current and MVA Calculations, Fault Levels, Application of Series Reactors, Numerical Problems. Symmetrical Component Theory: Symmetrical Component Transformation, Positive, Negative and Zero Sequence Components: Voltages, Currents and Impedances. Sequence Networks: Positive, Negative and Zero sequence Networks, Numerical Problems. Unsymmetrical Fault Analysis: LG, LL, LLG faults with and without Fault Impedance, Numerical Problems.

UNIT – III

POWER FLOW STUDIES-I

Necessity of Power Flow Studies – Data for Power Flow Studies – Derivation of Static Load Flow Equations – Load Flow Solutions using Gauss Seidel Method: Acceleration Factor, Load Flow Solution with and without P-V Buses, Algorithm and Flowchart. Numerical Load flow Solution for Simple Power Systems (Max. 3-Buses): Determination of Bus Voltages, Injected Active and Reactive Powers (Sample One Iteration only) and Finding Line Flows/Losses for the given Bus Voltages.

UNIT – IV

POWER FLOW STUDIES-II

Newton Raphson Method in Rectangular and Polar Co-Ordinates Form: Load Flow Solution with or without PV Buses- Derivation of Jacobian Elements, Algorithm and Flowchart. Decoupled and Fast Decoupled Methods.- Comparison of Different Methods – DC Load Flow

UNIT – V

POWER SYSTEM STABILITY ANALYSIS

Elementary Concepts of Steady State, Dynamic and Transient Stabilities - Description of: Steady State Stability Power Limit, Transfer Reactance, Synchronizing Power Coefficient, Power Angle Curve and Determination of Steady State Stability and Methods to Improve Steady State Stability - Derivation of Swing Equation - Determination of Transient Stability by Equal Area Criterion, Application of Equal Area Criterion, Critical Clearing Angle Calculation. Solution of Swing Equation by 4th Order Runge Kutta Method (up to 2 iterations) - Methods to improve Stability - Application of Auto Reclosing and Fast Operating Circuit Breakers.

Course Outcomes: At the end of the course the student should be able to:

- Form the Z_{bus} and Y_{bus} of a given power system network
- Compare different methods used for obtaining load flow solution
- Conduct load flow studies on a given system
- Make fault calculations for various types of faults
- Determine the transient stability by equal area criterion
- Determine steady state stability power limit
- Distinguish between different types of buses used in load flow solution

TEXT BOOKS:

1. Power Systems Analysis, Grainger and Stevenson, Tata Mc Graw-hill, 2005.
2. Modern Power system Analysis 2nd edition, I.J.Nagrath & D.P.Kothari: Tata McGraw-Hill Publishing Company, 2003.

REFERENCE BOOKS:

1. Computer Techniques in Power System Analysis 2nd Edition,, M A Pai, TMH, 2005.
2. Computer Techniques and Models in Power Systems, K. Uma Rao, I. K. International, 2007.
3. Electric Power Systems 1st Edition, S. A. Nasar, Schaum's Outline Series, TMH, 1997.
4. Computer Methods in Power System Analysis, E. I. Stagg and El-Abiad, Tata Mc Graw Hill, 1969.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

| | | | | |
|----------------------------------|----------|----------|----------|----------|
| B. Tech III-II Sem. (EEE) | L | T | P | C |
| | 3 | 1 | 0 | 3 |

**15A02604 NEURAL NETWORKS & FUZZY LOGIC
(CBCC-I)**

Course Objective: The objectives of the course are to make the students learn about:

- Importance of AI techniques in engineering applications
- Artificial Neural network and Biological Neural Network concepts
- ANN approach in various Electrical Engineering problems
- Fuzzy Logic and Its use in various Electrical Engineering Applications

UNIT – I

INTRODUCTION TO ARTIFICIAL INTELLIGENCE

Introduction and motivation – Approaches to AI – Architectures of AI – Symbolic Reasoning System – Rule based Systems – Knowledge Representation – Expert Systems.

UNIT – II

ARTIFICIAL NEURAL NETWORKS

Basics of ANN - Comparison between Artificial and Biological Neural Networks – Basic Building Blocks of ANN – Artificial Neural Network Terminologies – McCulloch Pitts Neuron Model – Learning Rules – ADALINE and MADALINE Models – Perceptron Networks – Back Propagation Neural Networks – Associative Memories.

UNIT – III

ANN APPLICATIONS TO ELECTRICAL SYSTEMS

ANN approach to: Electrical Load Forecasting Problem – System Identification – Control Systems – Pattern Recognition.

UNIT – IV

FUZZY LOGIC

Classical Sets – Fuzzy Sets – Fuzzy Properties and Operations – Fuzzy Logic System – Fuzzification – Defuzzification – Membership Functions – Fuzzy Rule base – Fuzzy Logic Controller Design.

UNIT – V

FUZZY LOGIC APPLICATIONS TO ELECTRICAL SYSTEMS

Fuzzy Logic Implementation for Induction Motor Control – Switched Reluctance Motor Control – Fuzzy Excitation Control Systems in Automatic Voltage Regulator - Fuzzy Logic Controller in an 18 Bus Bar System.

Course Outcomes: The students should acquire awareness about:

- Approaches and architectures of Artificial Intelligence
- Artificial Neural Networks terminologies and techniques
- Application of ANN to Electrical Load Forecasting problem, Control system problem
- Application of ANN to System Identification and Pattern recognition
- The development of Fuzzy Logic concept
- Use of Fuzzy Logic for motor control and AVR operation
- Use of Fuzzy Logic controller in an 18 bus bar system

Text Books:

1. S. N. Sivanandam, S. Sumathi and S. N. Deepa, "Introduction to Neural Networks using MATLAB", McGraw Hill Edition, 2006.
2. Timothy J. Ross, "Fuzzy Logic with Engineering Applications", Third Edition, WILEY India Edition, 2012.

References:

1. S. N. Sivanandam, S. Sumathi and S. N. Deepa, "Introduction to Fuzzy Logic using MATLAB", Springer International Edition, 2013.
2. Yung C. Shin and Chengying Xu, "Intelligent System – Modeling, Optimization & Control, CRC Press, 2009.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B. Tech III-II Sem. (EEE) **L T P C**
3 1 0 3

**15A02605 PROGRAMMABLE LOGIC CONTROLLER AND ITS APPLICATIONS
(CBCC-I)**

Course Objectives: The objectives of the course are to make the students learn about:

- PLC and its basics, architecture, connecting devices and programming
- Implementation of Ladder logic for various Industrial applications
- Designing of control circuits for various applications
- PLC logic and arithmetic operations

UNIT-I

PLC Basics: PLC System, I/O Modules and Interfacing, CPU Processor, Programming Equipment, Programming Formats, Construction of PLC Ladder Diagrams, Devices Connected To I/O Modules. PLC Programming: Input Instructions, Outputs, Operational Procedures, Programming Examples Using Contacts and Coils. Drill Press Operation.

UNIT-II

Digital Logic Gates, Programming in the Boolean Algebra System, Conversion Examples. Ladder Diagrams for Process Control: Ladder Diagrams & Sequence Listings, Ladder Diagram Construction and Flowchart for Spray Process System.

UNIT-III

PLC Registers: Characteristics of Registers, Module Addressing, Holding Registers, Input Registers, Output Registers. PLC Functions: Timer Functions & Industrial Applications, Counter Function & Industrial Applications, Arithmetic Functions, Number Comparison Functions, Number Conversion Functions

UNIT-IV

Data Handling Functions: SKIP, Master Control Relay, Jump, Move, FIFO, FAL, ONS, CLR & Sweep Functions and Their Applications. Bit Pattern and Changing a Bit Shift Register, Sequence Functions and Applications, Controlling of Two-Axis & Three Axis Robots With PLC, Matrix Functions.

UNIT-V

Analog PLC Operation, Types of PLC Analog Modules and Systems, PLC Analog Signal Processing, BCD or Multibit data Processing, Analog output application examples, PID Modules, PID Tuning, Typical PID Functions, PLC Installation, Troubleshooting and Maintenance.

Course Outcomes: The student should be able to:

- Program a PLC for a given application
- Implement Ladder logic for various Industrial applications
- Design control circuits for various applications

TEXT BOOKS:

1. Programmable Logic Controllers- Principles and Applications by John W. Webb & Ronald A. Reiss, Fifth Edition, ELSEVIER Ltd., 2009.
2. Programmable Logic Controllers 5th Edition, William Bolton, Newnes, ELSEVIER Ltd., 2009.

REFERENCES:

1. Programmable Logic Controllers: An Emphasis on design & application, Kelvin T. Erickson, Dogwood Valley Press, 2011.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B. Tech III-II Sem. (EEE)

| L | T | P | C |
|---|---|---|---|
| 3 | 1 | 0 | 3 |

**15A02606 OPTIMIZATION TECHNIQUES
(CBCC-I)**

Course Objectives :

The objectives of the course are to make the students learn about:

- The basic concepts of optimization and classification of optimization problems.
- Different classical Optimization techniques, linear programming, unconstrained and constrained nonlinear programming.
- Soft Computing methods – GA & PSO

UNIT-I

INTRODUCTION AND CLASSICAL OPTIMIZATION TECHNIQUE

Statement of an Optimization Problem- Design Vector- Design Constraints- Constraints Surface – Objective Function- Objective Function Surfaces- Classification of Optimization Problems. Classical Optimization Techniques- Single Variable Optimization- Multi Variable Optimization Without Constraints- Necessary and Sufficient Conditions for Minimum/Maximum- Multi Variable Optimization With Equality Constraints Solution by Method of Lagrange Multipliers- Multi Variable Optimization with Inequality Constraints – Kuhn- Tucker Conditions

UNIT-II

LINEAR PROGRAMMING

Standard Form of Linear Programming Problem- Geometry of Linear Programming Problems- Definitions and Theorems- Solution of a System of Linear Simultaneous Equations- Pivotal Reduction of a General System of Equations- Motivation to The Simplex Method- Simplex Algorithm – Revised Simplex Method – Two Phase Simplex Method - Initial Basic Feasible Solution by North- West Corner Rule, Approximation Method.

UNIT-III

UNCONSTRAINED NONLINEAR PROGRAMMING

One-Dimensional Minimization Methods: Classification, Fibonacci Method and Quadratic Interpolation Method- Unconstrained Optimization Techniques- Univariate Method, Powell's Method, Steepest Descent Method, Newtons Method.

UNIT-IV

CONSTRAINED NONLINEAR PROGRAMMING

Characteristics of a Constrained Problem, Classification, Basic Approach of Penalty Function Method; Basic Approaches of Interior and Exterior Penalty Function Methods, Introduction to Convex Programming Problem

UNIT-V

SOFT COMPUTING METHODS

Evolutionary programming methods - Introduction to Genetic Algorithms (GA)– Control parameters –Number of generation, population size, selection, reproduction, crossover and mutation – Operator selection criteria – Simple mapping of objective function to fitness function – constraints – Genetic algorithm steps – Stopping criteria –Simple examples.

Swarm intelligence programming methods - Basic Partial Swarm Optimization – Method – Characteristic features of PSO procedure of the global version – Parameters of PSO (Simple PSO algorithm – Operators selection criteria – Fitness function constraints)

Course Outcomes:

The student should be able to:

- Develop an objective function and obtain solution for multivariable optimization problem with equality/Inequality constraints
- Apply linear programming techniques for problem solving
- Apply nonlinear programming techniques for unconstrained/constrained optimization
- Use soft computing techniques to solve optimization problems

TEXT BOOKS:

1. Engineering optimization: Theory and practice 3rd edition, S.S.Rao, New Age International (P) Limited, 1998.
2. Optimization Methods in Operations Research and systems Analysis 3rd edition, K.V.Mital and C.Mohan, New Age International (P) Limited, 1996.
3. Soft Computing with Matlab Programming by N.P.Padhy&S.P.Simson, Oxford University Press – 2015

REFERENCE BOOKS:

1. Operations Research, Dr.S.D.Sharma, S.Chand & Sons, 2001.
2. Operation Research: An Introduction 6th edition, H.A.Taha, PHI , 2003.
3. Optimization for Engineering Design – Algorithms and Examples, Kalyanmoy Deb, 2nd Edition, PHI, 2014.
4. Soft Computing Advances and Applications, B. K. Tripathy and J. Anuradha, CENGAGE Learning, 2015.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B. Tech III-II Sem. (EEE)

| L | T | P | C |
|---|---|---|---|
| 3 | 1 | 0 | 3 |

15A01608

**INTELLECTUAL PROPERTY RIGHTS
(CBCC-I)**

COURSE OBJECTIVE:

This course introduces the student to the basics of Intellectual Property Rights, Copy Right Laws Trade Marks and Issues related to Patents. The overall idea of the course is to help and encourage the student for startups and innovations.

UNIT – I

Introduction To Intellectual Property: Introduction, Types Of Intellectual Property, International Organizations, Agencies And Treaties, Importance Of Intellectual Property Rights.

UNIT – II

Trade Marks : Purpose And Function Of Trade Marks, Acquisition Of Trade Mark Rights, Protectable Matter, Selecting And Evaluating Trade Mark, Trade Mark Registration Processes.

UNIT – III

Law Of Copy Rights : Fundamental Of Copy Right Law, Originality Of Material, Rights Of Reproduction, Rights To Perform The Work Publicly, Copy Right Ownership Issues, Copy Right Registration, Notice Of Copy Right, International Copy Right Law.

Law Of Patents : Foundation Of Patent Law, Patent Searching Process, Ownership Rights And Transfer

UNIT – IV

Trade Secrets : Trade Secrete Law, Determination Of Trade Secrete Status, Liability For Misappropriations Of Trade Secrets, Protection For Submission, Trade Secrete Litigation.

Unfair Competition : Misappropriation Right Of Publicity, False Advertising.

UNIT – V

New Developments Of Intellectual Property: New Developments In Trade Mark Law ; Copy Right Law, Patent Law, Intellectual Property Audits.

International Overview On Intellectual Property, International – Trade Mark Law, Copy Right Law, International Patent Law, International Development In Trade Secrets Law.

TEXT BOOKS & REFERENCES:

1. Intellectual Property Rights, Deborah. E. Bouchoux, Cengage Learning.
2. Intellectual Property Rights– Unleashmy The Knowledge Economy, Prabuddha Ganguli, Tate Mc Graw Hill Publishing Company Ltd.,

Course Outcomes:

On completion of this course, the student will have an understanding of the following:

- a) *Intellectual Property Rights and what they mean*
- b) *Trade Marks and Patents and how to register them*
- c) *Laws Protecting the Trade Marks and Patents*
- d) *Copy Right and laws related to it.*

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

| | | | | |
|---|----------|----------|----------|----------|
| B. Tech III-II Sem. (EEE) | L | T | P | C |
| 15A04607 MICROPROCESSORS AND MICROCONTROLLERS LABORATORY | 0 | 0 | 4 | 2 |

Part A : 8086 Microprocessor Programs using NASM/8086 microprocessor kit.

1. Introduction to MASM Programming.
2. Programs using arithmetic and logical operations
3. Programs using string operations and Instruction prefix: Move Block, Reverse string, Sorting, String comparison
4. Programs for code conversion
5. Multiplication and Division programs
6. Sorting and multi byte arithmetic
7. Programs using CALL and RET instructions

Part B Embedded C Experiments using MSP430 Microcontroller

1. Interfacing and programming GPIO ports in C using MSP430 (blinking LEDs , push buttons)
2. Usage of Low Power Modes: (Use MSPEXP430FR5969 as hardware platform and demonstrate the low power modes and measure the active mode and standby mode current)
3. Interrupt programming examples through GPIOs
4. PWM generation using Timer on MSP430 GPIO
5. Interfacing potentiometer with MSP430
6. PWM based Speed Control of Motor controlled by potentiometer connected to MSP430 GPIO
7. Using ULP advisor in Code Composer Studio on MSP430
8. Low Power modes and Energy trace++:
 - a. Enable Energy Trace and Energy Trace ++ modes in CCS
 - b. Compute Total Energy, and Estimated lifetime of an AA battery.

Note : Any six experiment from Part A and Six experiments from Part B are to be conducted

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

| | | | | |
|---|----------|----------|----------|----------|
| B. Tech III-II Sem. (EEE) | L | T | P | C |
| 15A02607 POWER ELECTRONICS AND SIMULATION LABORATORY | 0 | 0 | 4 | 2 |

Course Objectives: The student will understand:

- The characteristics of power electronic devices with gate firing circuits
- Various forced commutation techniques
- The operation of single-phase voltage controller, converters and Inverters circuits with R and RL loads
- Analyze the TPS7A4901, TPS7A8300 and TPS54160 buck regulators

Any Eight of the Experiments in Power Electronics Lab

1. Gate Firing Circuits for SCRs
2. Single Phase AC Voltage Controller with R and RL Loads
3. DC Jones Chopper with R and RL Loads
4. Forced Commutation Circuits (Class A, Class B, Class C, Class D and Class E)
5. Three phase fully controlled Bridge converter with R- load
6. Single Phase Parallel, Inverter with R and RL Loads
7. Single phase Cycloconverter with R and RL loads
8. Single Phase Series Inverter with R and RL Loads
9. Single Phase Dual Converter with RL Loads
10. Illumination control / Fan control using TRIAC

Any Four Experiments of the following (1, 2, 3, A, B, C):

1. Using TPS7A4901 and TPS7A8300, study-
 - a. Impact of line and load conditions on drop out voltage
 - b. Impact of line and load conditions on efficiency
 - c. Impact of capacitor on PSRR
 - d. Impact of output capacitor on load-transient response
2. Study of DC-DC Buck converter
 - a) Investigate how the efficiency of a TPS54160 buck regulator depends on the line and load conditions and on the switching frequency.
 - b) Analyze the influence of switching frequency f_s and of capacitance C and resistance ESR of the input and output capacitors on steady-state waveforms of TPS54160 buck regulator.

3. Analyze how the switching frequency f_s , the DC accuracy and the line noise rejection of the hysteretic buck regulator LM3475 depend on line voltage, the load current, the characteristics of the output capacitor and the impact of speed-up capacitor.

WEBENCH EXPERIMENTS:

- A. Design of a Low cost Boost Converter to derive 12V, 100mA from 5V USB
- B. Design of a low cost and power efficient Buck Converter that could be used as a USB charger for mobile devices deriving its power from an automotive battery.
- C. Design of a low cost synchronous buck converter.

Course Outcomes: Student should be able to:

- Test the turn on –turn off characteristics of various power electronic devices.
- Test and analyze firing circuits for SCRs
- Test different types of voltage controllers, converters and Inverters with R and RL loads
- Analyze the TPS7A4901, TPS7A8300 and TPS54160 buck regulators

REFERENCES:

1. PMLK BUCK Lab manual - <http://www.ti.com/lit/ug/ssqu007/ssqu007.pdf>
2. PMLK LDO Lab manual - <http://www.ti.com/lit/ug/ssqu006/ssqu006.pdf>
3. WEBENCH – www.ti.com/webench

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

B. Tech III-II Sem. (EEE)

| L | T | P | C |
|---|---|---|---|
| 0 | 0 | 2 | 0 |

**15A52602 ADVANCED ENGLISH LANGUAGE COMMUNICATION SKILLS
(AELCS) LAB (Audit Course)**

1. INTRODUCTION

With increased globalization and rapidly changing industry expectations, employers are looking for the wide cluster of skills to cater to the changing demand. The introduction of the Advanced Communication Skills Lab is considered essential at 3rd year level. At this stage, the students need to prepare themselves for their careers which may require them to listen to, read, speak and write in English both for their professional and interpersonal communication in the globalised context.

The proposed course should be a laboratory course to enable students to use 'good' English and perform the following:

- Gathering ideas and information and to organise ideas relevantly and coherently.
- Engaging in debates.
- Participating in group discussions.
- Facing interviews.
- Writing project/research reports/technical reports.
- Making oral presentations.
- Taking part in social and professional communication.

2. OBJECTIVES:

This Lab focuses on using multi-media instruction for language development to meet the following targets:

- To improve the students' fluency in English, through a well-developed vocabulary and enable them to listen to English spoken at normal conversational speed by educated English speakers and respond appropriately in different socio-cultural and professional contexts.
- Further, they would be required to communicate their ideas relevantly and coherently in writing.
- To prepare all the students for their placements.

3. SYLLABUS:

The following course content to conduct the activities is prescribed for the Advanced English Communication Skills (AECS) Lab:

UNIT-I: COMMUNICATION SKILLS

1. Reading Comprehension
2. Listening comprehension
3. Vocabulary Development
4. Common Errors

UNIT-II: WRITING SKILLS

1. Report writing
2. Resume Preparation
3. E-mail Writing

UNIT-III: PRESENTATION SKILLS

1. Oral presentation
2. Power point presentation
3. Poster presentation

UNIT-IV: GETTING READY FOR JOB

1. Debates
2. Group discussions
3. Job Interviews

UNIT-V: INTERPERSONAL SKILLS

1. Time Management
2. Problem Solving & Decision Making
3. Etiquettes

4. LEARNING OUTCOMES:

- Accomplishment of sound vocabulary and its proper use contextually
- Flair in Writing and felicity in written expression.
- Enhanced job prospects.
- Effective Speaking Abilities

5. MINIMUM REQUIREMENT:

The Advanced English Communication Skills (AECS) Laboratory shall have the following infra-structural facilities to accommodate at least 60 students in the lab:

- Spacious room with appropriate acoustics.
- Round Tables with movable chairs
- Audio-visual aids
- LCD Projector
- Public Address system
- P – IV Processor, Hard Disk – 80 GB, RAM–512 MB Minimum, Speed – 2.8 GHZ
- T. V, a digital stereo & Camcorder
- Headphones of High quality

6. SUGGESTED SOFTWARE:

The software consisting of the prescribed topics elaborated above should be procured and G

1. **Walden Infotech: Advanced English Communication Skills Lab**
2. **K-VAN SOLUTIONS-Advanced English Language Communication Skills lab**
3. **DELTA's key to the Next Generation TOEFL Test: Advanced Skills Practice.**
4. **TOEFL & GRE**(KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)
5. **Train2success.com**

7. BOOKS RECOMMENDED:

1. **Objective English for Competitive Exams**, Hari Mohana Prasad, 4th edition, Tata Mc Graw Hill.
2. **Technical Communication** by Meenakshi Raman & Sangeeta Sharma, O U Press 3rd Edn. 2015.
3. **Essay Writing for Exams, Audrone Raskauskiene, Irena Ragaisience & Ramute Zemaitience,OUP, 2016**
4. **Soft Skills for Everyone**, Butterfield Jeff, Cengage Publications, 2011.
5. **Management Shapers Series** by Universities Press (India) Pvt Ltd., Himayatnagar, Hyderabad 2008.
6. **Campus to Corporate**, Gangadhar Joshi, Sage Publications, 2015
7. **Communicative English**,E Suresh Kumar & P.Sreehari, Orient Blackswan, 2009.
8. **English for Success in Competitive Exams**, Philip Sunil Solomon OUP, 2015